

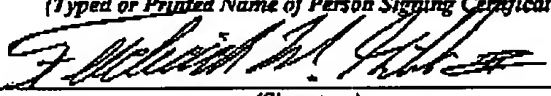
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PAGE 01

<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b> Applicant(s): Allen et al.			Docket No. <b>BUR920030092US1</b>	
Application No. 10/604,987	Filing Date 08/29/03	Examiner Siek, Vuthe	Group Art Unit 2822	
Invention: <b>THE USE OF A LAYOUT-OPTIMIZATION TOOL TO INCREASE THE YIELD AND RELIABILITY OF VLSI DESIGNS</b>				
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Application Number	10/604,987
Filing Date	08/29/03
First Named Inventor	Allen et al.
Art Unit	2822
Examiner Name	Siek, Vuthe
Attorney Docket Number	BUR920030092US1

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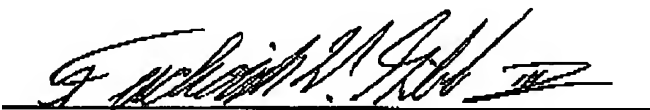
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301-261-8071

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